

S.N. 10/ 049,792

Please cancel claims 4, 7, 14 and 19-21 and
amend claims 1 to 3, 5, and 8-10 as follows:

1. (Amended) An array substrate comprising:
a display area in which pixel electrodes are formed,
a scanning line formed of a low resistivity metal, said
scanning line being arranged between then pixel electrodes,
a signal line formed of a high melting point metal selected
from the group consisting of chrome, molybdenum, tantalum and
alloys thereof, said signal line crossing over the scanning line
interposing an insulating layer therebetween,
a terminal to which a scanning signal is applied, and
an extended scanning line [formed from a conductive film]
for connecting the scanning line with the terminal, said extended
scanning line being formed only of the same conductive film as for
said signal line.

2. (Amended) The array substrate of claim 1 comprising:
an auxiliary capacitance line arranged parallel to the
scanning line,
a collected auxiliary capacitance line arranged
in parallel to the signal line and electrically connected
to the auxiliary capacitance line,
a terminal to which a common signal is applied, and
an extended auxiliary capacitance line [formed from a
conductive film] for connecting the collected auxiliary capacitance
line with the terminal for the common signal, said extended
auxiliary capacitance line being formed only of the same conductive
film as for said signal line.

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3. (Amended) An array substrate comprising:
a display area in which pixel electrodes are formed,
a scanning line formed of a low resistivity metal, said scanning line being arranged between the pixel electrodes,
an auxiliary capacitance line arranged in parallel to the scanning line,
a signal line formed of a high melting point metal selected from the group consisting of chrome, molybdenum, tantalum and alloys thereof, said signal line crossing over the scanning line and the auxiliary capacitance line interposing an insulating layer therebetween,
a collected auxiliary capacitance line arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line,
a terminal to which a common signal is applied, and
an extended auxiliary capacitance line for connecting the collected auxiliary capacitance line with the terminal, said extended auxiliary capacitance line being formed only of the same conductive film as for said signal line.

5. (amended) The array substrate of claim 1, wherein the extended scanning line is formed only of the same conductive film as for the pixel electrodes, instead of the same conductive film as for said signal line.

8. (amended) The array substrate of claim 2, wherein the extended auxiliary capacitance line is formed only of the same conductive film as for the pixel electrodes, instead of the same conductive film as for the signal line.

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9. (amended) The array substrate of claim 8, wherein
the extended auxiliary capacitance line is electrically connected
to the collected auxiliary capacitance line at the neighborhood of
the display area and electrically connected to the terminal for the
common signal at the neighborhood of the terminal.

10. (Amended) The array substrate of claim 2, wherein
the auxiliary capacitance line,
the collected auxiliary capacitance line and
the scanning line are formed from the conductive film of same layer.
